

FIG. 1A (PRIOR ART)

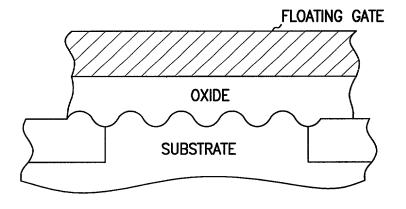


FIG. 1B (PRIOR ART)

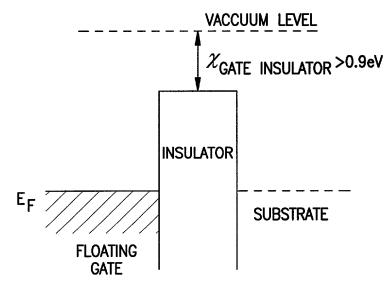


FIG. 1C (PRIOR ART)

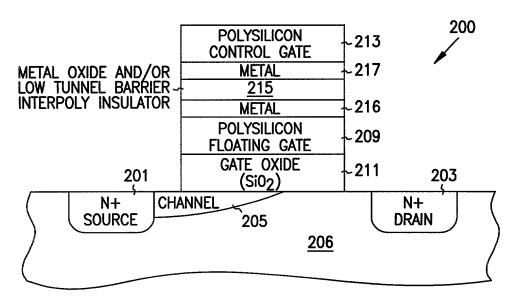
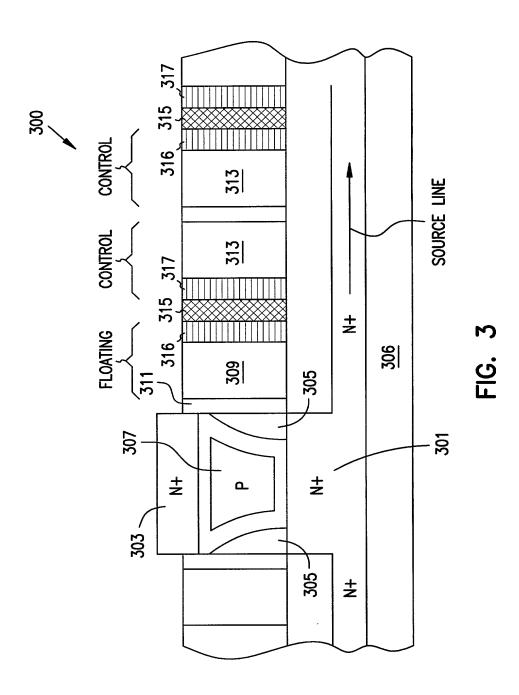


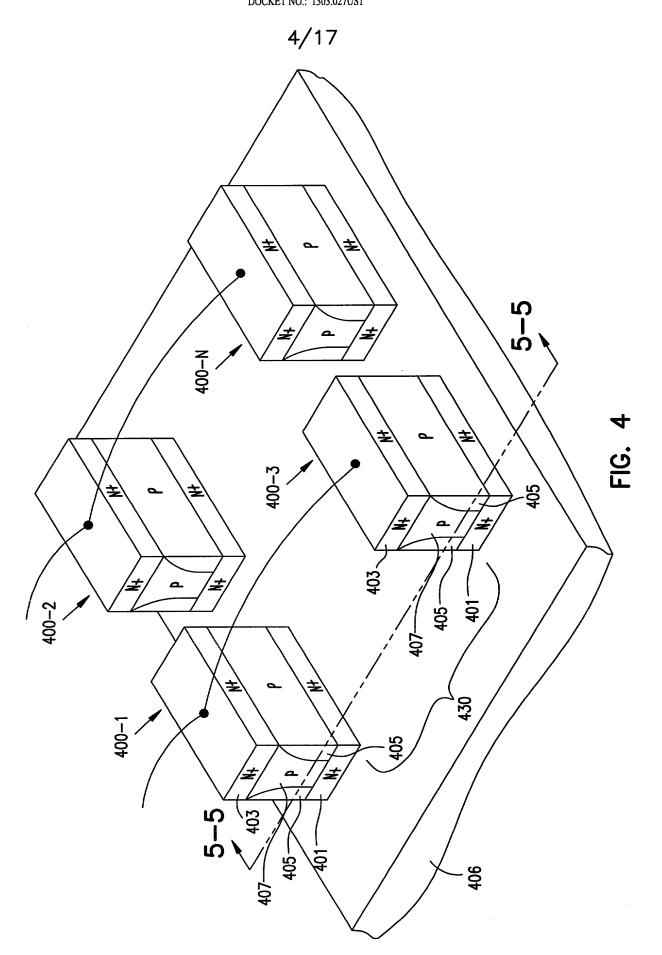
FIG. 2

INVENTORS NAME: Leonard Forbes DOCKET NO.: 1303.027US1

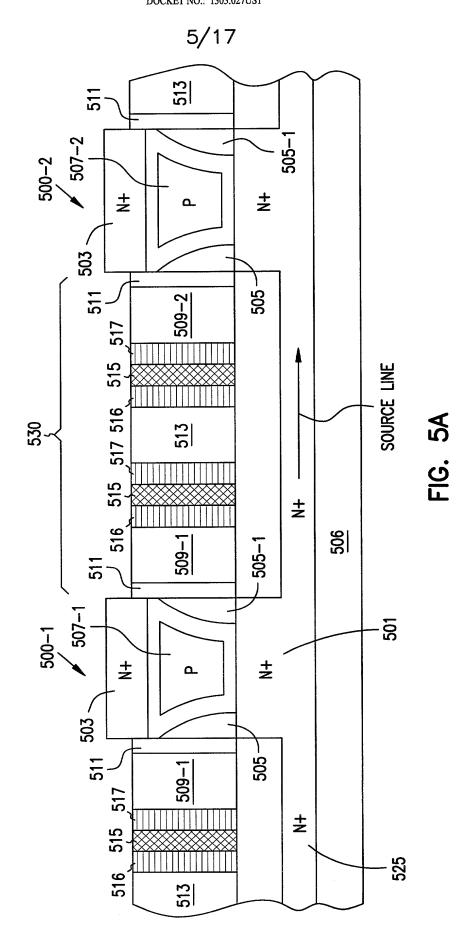


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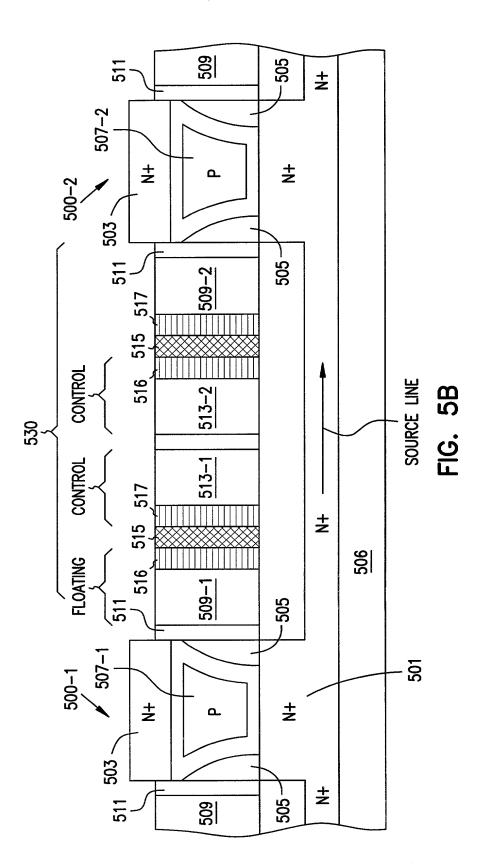
TITLE: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS INVENTORS NAME: Leonard Forbes DOCKET NO.: 1303.027US1



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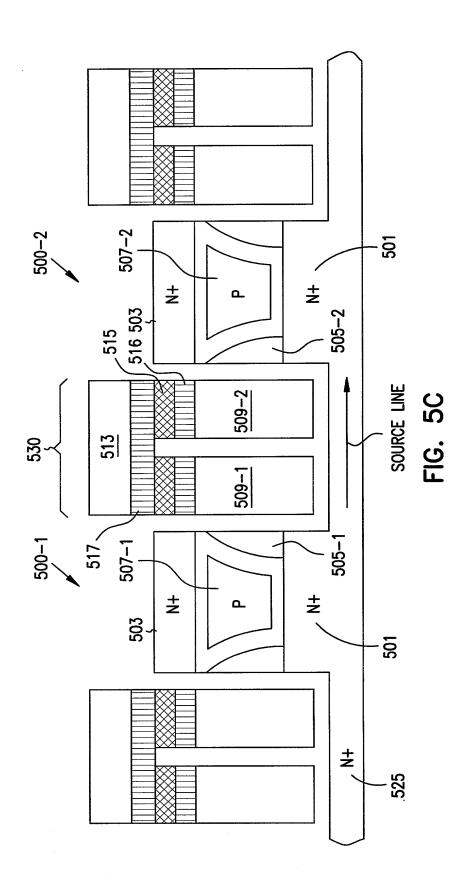
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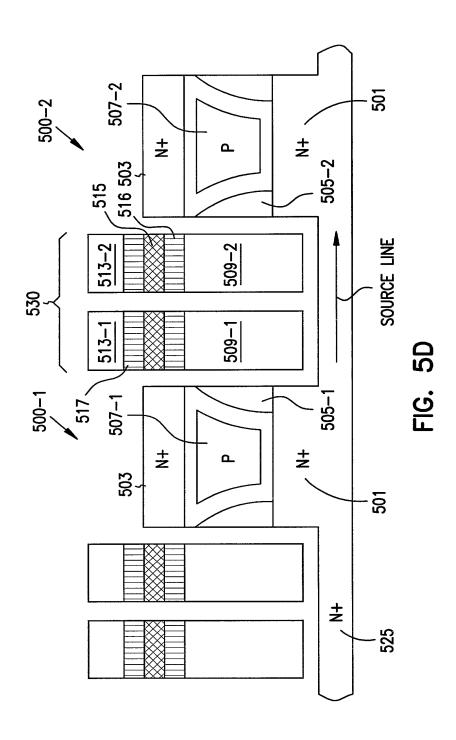
DOCKET NO.: 1303.027US1

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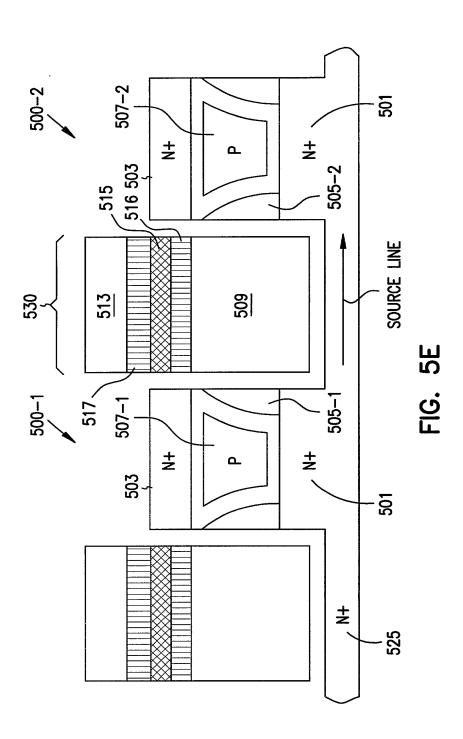


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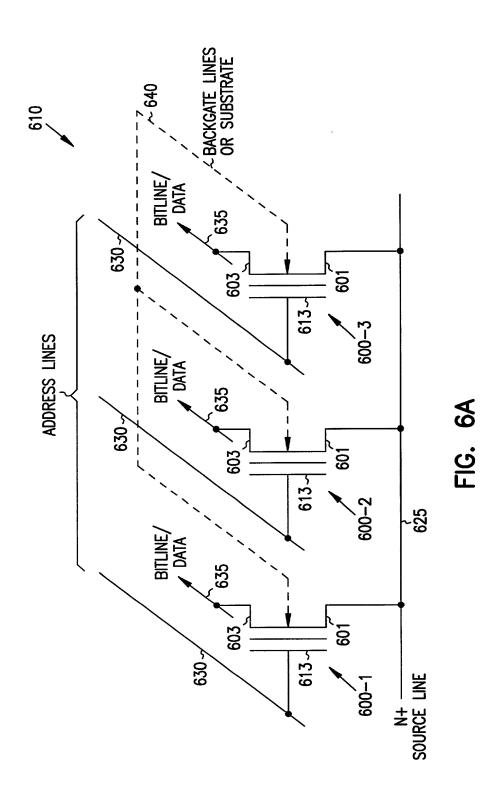


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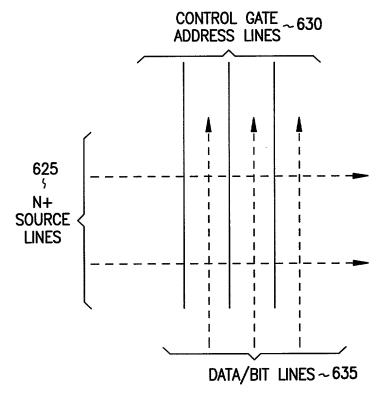
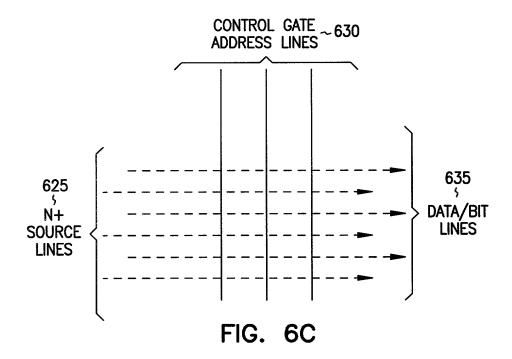
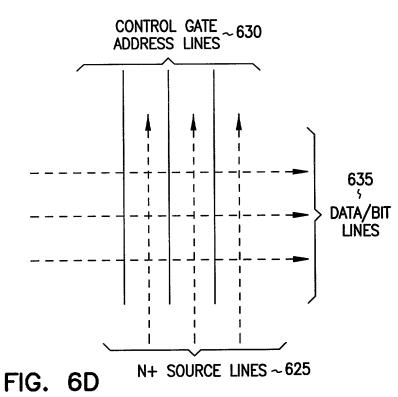
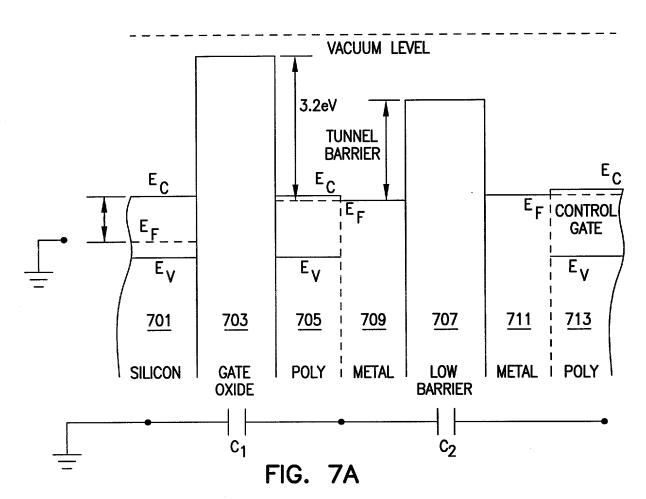


FIG. 6B











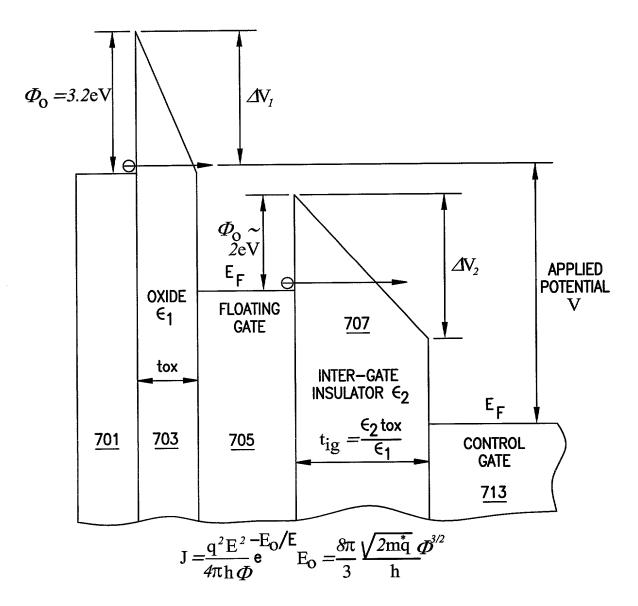


FIG. 7B

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TITLE: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS INVENTORS NAME: Leonard Forbes



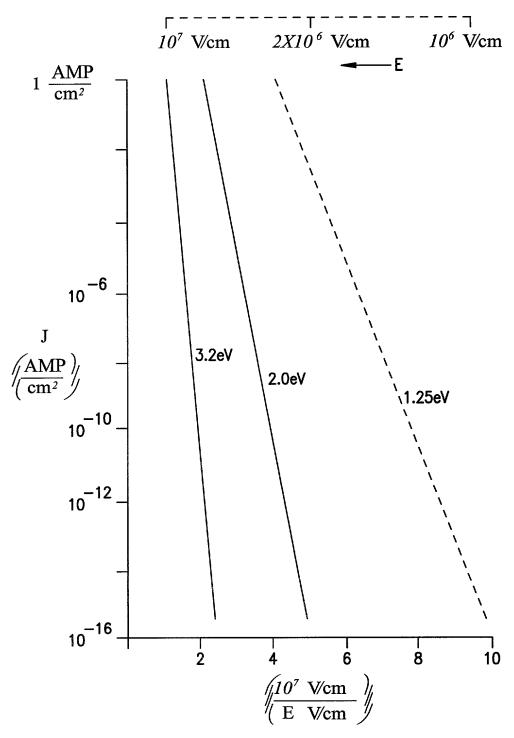


FIG. 7C

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DOCKET NO.: 1303.027US1

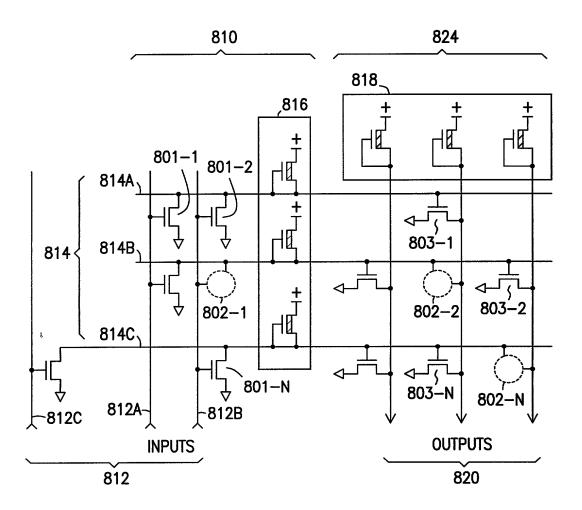


FIG. 8

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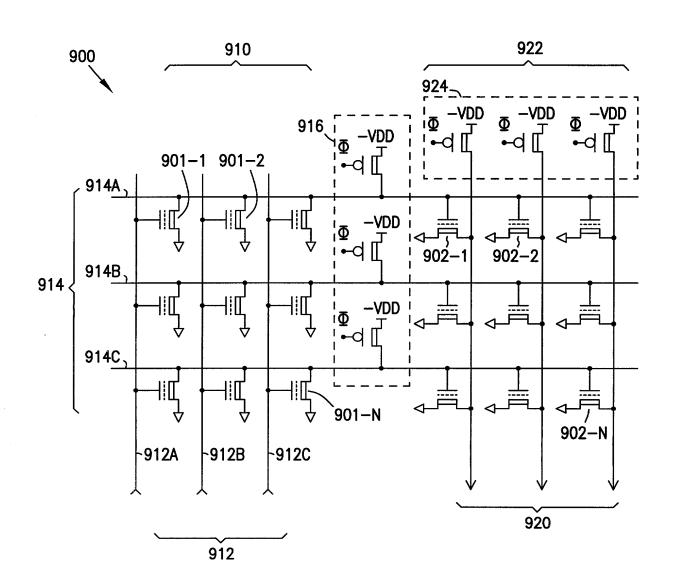


FIG. 9

FIG. 10